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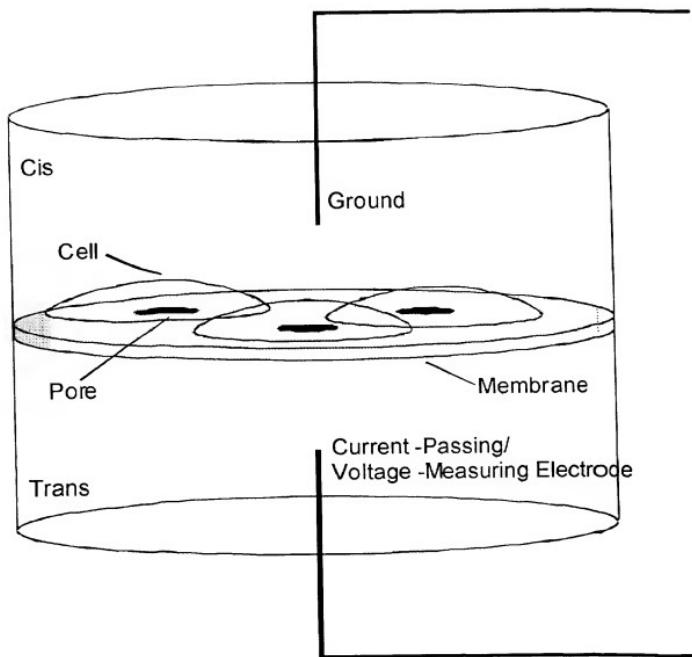


FIG. 1

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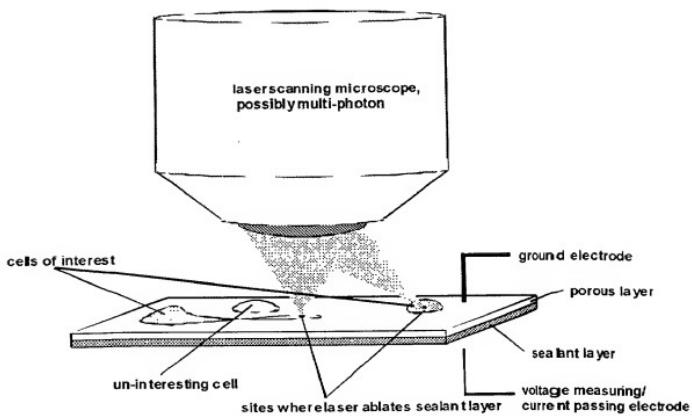
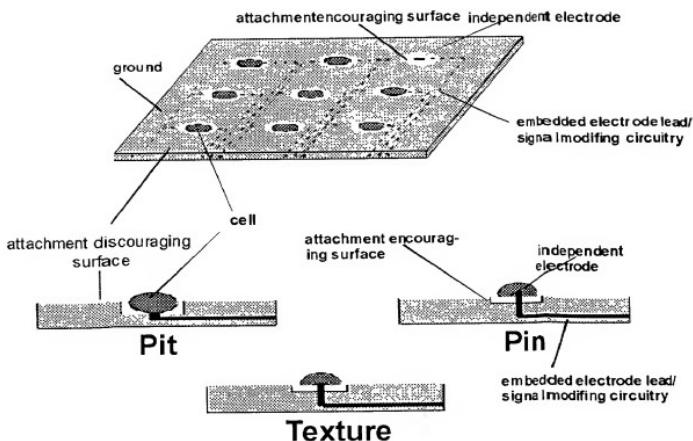


FIG. 2

Wafer/Chip



Texture

FIG. 3